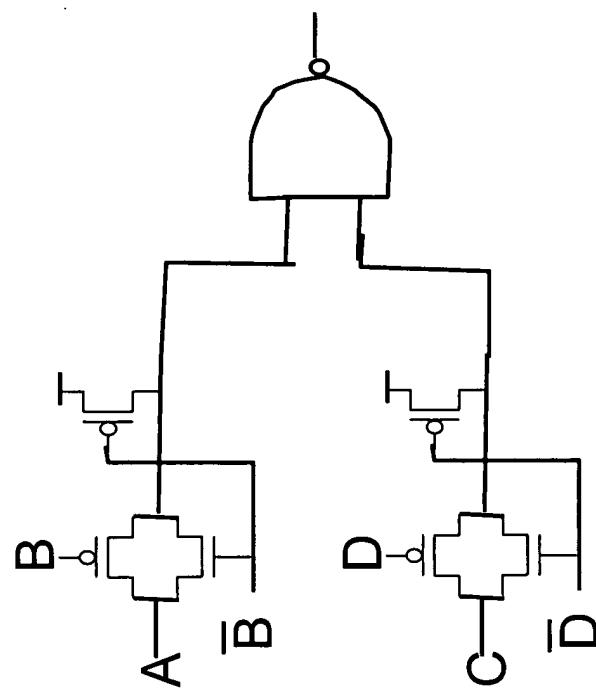
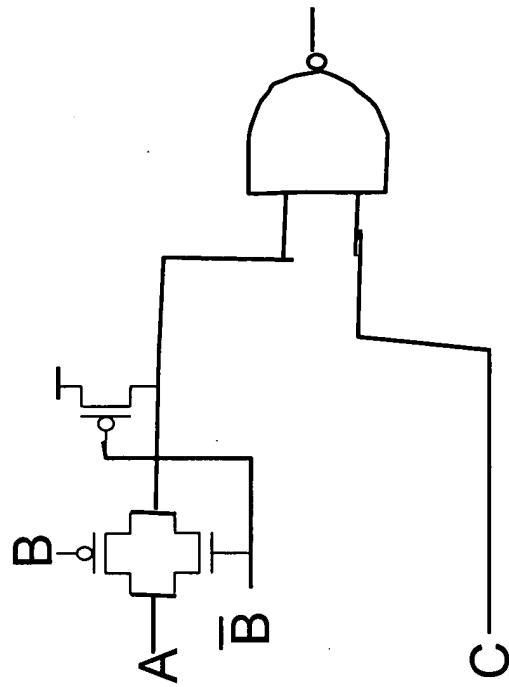


Example



The inventive OR circuit directly produces an OR signal, without the use of an inverter.

The produced OR signal is used directly with static CMOS complex logic gates such as a NAND circuit, shown above to perform more complex functions using lesser number of transistors with faster delay and smaller power.